

Overview

XJAnalyser is a visual analysis and debugging tool for devices in your JTAG chain. It provides instant chain verification as part of the simple 3-step set-up, and then gives you an interactive graphical view of the pins on your JTAG devices.

You can group pins into busses for easier control, and quickly generate toggling signals to trace connections around your board—useful when verifying shorts or opens. XJAnalyser also supports the STAPL/JAM and SVF standards for programming JTAG devices in-system.

Graphical circuit debugging

When tracing a net around your board with an oscilloscope, set a pin on the net to toggle and capture the signal at different points. If you slip to another pin, you will instantly know that you are no longer tracing the signal of interest.

Quickly locate signals you are sending to a device. By monitoring pins with changing values you can, for instance, press a button and quickly locate and display the pin/ball it is connected to, even if there are many thousands of pins/balls on the devices in your chain.

See the section of the chain of interest. For devices with large numbers of pins/balls, the information can become overwhelming. XJAnalyser solves this problem by enabling you to zoom in on just the balls or pins that you are interested in. You can also display multiple views of the JTAG chain, showing different areas of interest.

Flexible control

Control the devices in your JTAG chain the way you want to. XJAnalyser offers three methods for controlling pins: directly through the graphic display, or by using the pin list or pin watch. The pin watch also allows you to group pins into busses; you can then write a value to a complete bus all at once.

JTAG chain interaction

The intuitive graphical interface allows rapid interaction with the devices in the JTAG chain without programming or booting any devices on your board.

Monitor the states of all the I/O pins in real-time and graphically set pins to output high, low or toggle as required.

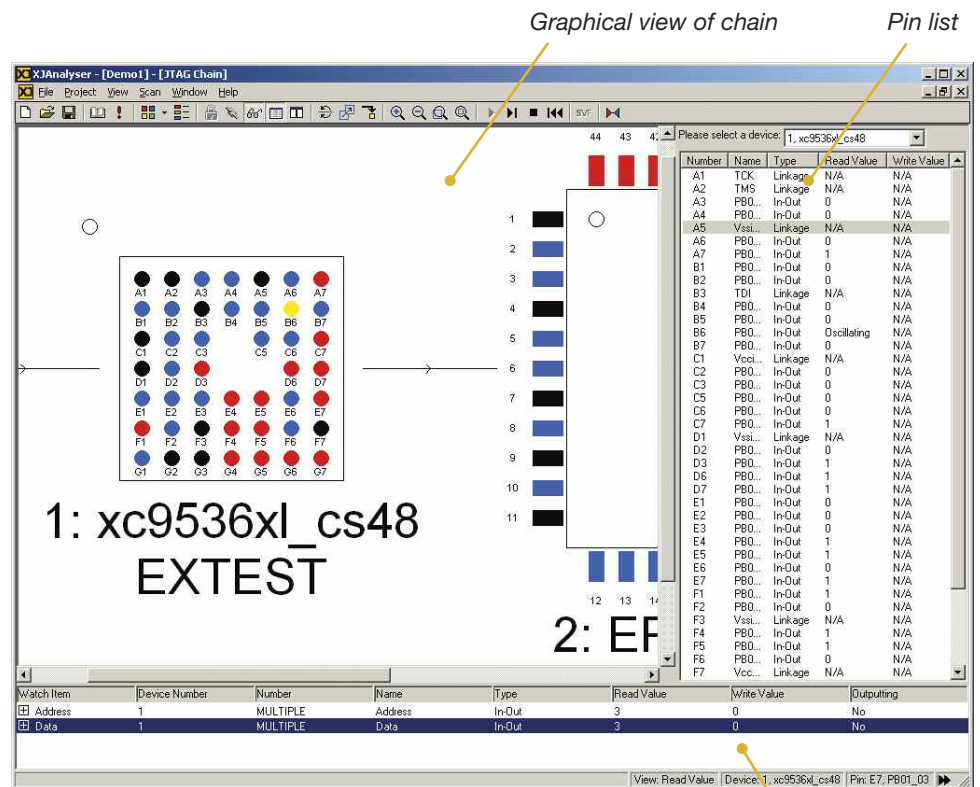
Simplify low-level access to any devices connected to a JTAG device by grouping

Key Benefits

- Allows you to increase yields — by setting pin values and tracing signals you can quickly debug your boards, even under BGAs
- You can reduce time to market by shortening the window from prototype to manufacture
- Save your budget by removing the need to have multiple download tools for programming devices

pins together into buses (e.g. “Data” or “Address”) and setting values using convenient units (Hex, Binary, Decimal).

Avoid damaging your board — XJAnalyser generates a warning if you attempt to drive any pin to a state that would put it in conflict with a value being driven to that net from a different source.



Graphical view of chain

Pin list

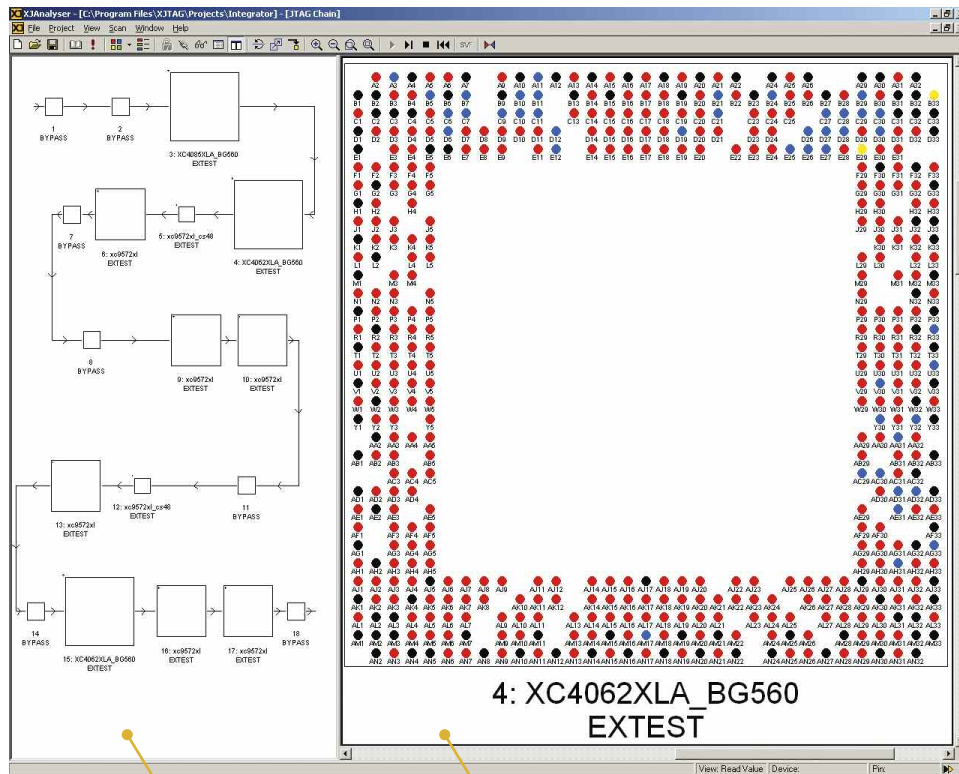
Watch window

Three-click wizard

XJAnalyser has a fast, simple setup wizard, to let you start testing and debugging your board straight away. All you have to do is select a JTAG header and a library containing appropriate BSDL files and you can start working with XJAnalyser. Even if you don't have a BSDL file, XJAnalyser will still work with the other devices.

CPLD programming

You can run STAPL /JAM and SVF files within XJAnalyser. These files are typically used to program devices such as CPLDs and FPGAs. Even if these files were created for a JTAG chain containing just a single device, XJAnalyser can run them on chains containing more devices.



Zoomed out view

Zoomed in view

Features

- Plug and play
- Able to test BGAs and fine-pitch devices
- Only BSDL files required to get the board up and running
- Set up pin states — e.g. low, high, toggling
- Trace shorts, opens and other signals
- Easy low-level access to device pins/busses
- Clear display of the pins/balls with variable zoom levels and split screen
- Quickly find and monitor changing pins
- Program devices with SVF and STAPL files
- Real-time interaction

XJTAG gives you more...

All of the features above are included when you buy XJAnalyser. The price you pay also includes:

- XJLink — the USB 2.0 to JTAG adapter required to connect your PC to the circuit under test
- Floating licence, held within the XJLink, so you can install the software on a number of machines
- Demonstration hardware
- Full tutorial

opinion

Jonathan Healy
Design Engineer
TTPCom

“XJTAG has exceeded our expectations. Its unparalleled speed, accuracy and ease-of-use have enabled us to shave days, if not weeks, off the development phase of our wireless reference platforms - important factors in the mobile phone industry where time-to-market is so critical.

It's important that we get these complex development boards up and running quickly and verify that each one is built according to the schematics as they are used extensively across the business and cost many thousands of dollars.”