KERAjet





KERAjet chooses XJTAG to Boost Testability of Specialised Printer Controllers

66 By finding out more about the extensive capabilities of the XJTAG boundary scan test system, which delivers benefits to prototype development as well as production testing, KERAjet of Vila-real in Spain is reaping the rewards of a sharper focus on DFT (Design for Test) in the early stages of board design.⁹⁹

KERAjet® España produces specialised inkjet printers capable of applying digital artwork directly to ceramic tiles. With major customers in the European ceramic heartlands of Spain, Portugal and Italy, KERAjet also exports to tile producers worldwide. The company's engineering teams are using the XJTAG boundary scan system to aid with design and test of the controller boards for its printers, and cites increased reliability and testability as two major advantages of incorporating the system into its processes.

The KERAjet P-140 or K-700/710 printers are four-colour machines capable of printing the complete image in a single pass. To manage the process, each inkjet printhead has its own dedicated controller board built around an FPGA featuring an embedded processor. Other circuitry includes A/D and D/A converters, SDRAM and Flash memory, and an Ethernet PHY chip allowing the board to communicate with the printer's main processor. Some printers may use up to 60 of these boards to apply the four-colour process over the full printable width.

To ensure a means to test the connections of the FPGA's BGA I/Os and other devices, which cannot be probed using conventional in-circuit test equipment, KERAjet engineers chose to use boundary scan testing. In practice, many other devices are connected to the JTAG chain, as well as the FPGA. This has significantly increased the test coverage that the team can achieve using boundary scan.

"In addition to successfully testing the FPGA, we are also testing SDRAM and FLASH memories," confirms Rafael Vicent, R&D Manager at KERAjet. "We are also emulating bus signals to test many other devices on the board. Moreover, by taking advantage of connections to devices such as A/D converters, XJTAG allows us to monitor the adjustable power supply and several other analog signals on the board."

The XJTAG system, which

several other JTAG test systems, maximises the power of boundary scan through features such as the high-level XJEase test language. The system's device-centric approach to test generation enables engineers to re-use proven tests in current and future projects. "XJTAG was clearly the most cost-effective solution for KERAjet," explains Rafael Vicent. "Not only were other systems more expensive, but XJTAG also offered the most powerful features that we really needed."

KERAjet selected after evaluating

XJTAG's built-in Design For Test (DFT) features have proved particularly valuable to KERAjet engineers. "Using XJTAG, we can produce a detailed DFT analysis at

Rafael Vicent R&D Manager KERAjet Espa

⁶⁶Boundary scan testing has proved to be a powerful technique, which has not only enhanced our test capabilities but has also influenced our design processes allowing us to achieve higher testability and reliability. The XJTAG system delivers outstanding value, combining powerful capabilities with ease of use through features such as the clear GUI, XJEase programming language and the device-centric philosophy that aids test generation.⁷⁷

⁶⁶XJTAG's built-in DFT features have proved particularly valuable by allowing us produce a detailed DFT analysis at the click of a button even before hardware is ready. We can design for very high levels of testability.³³ the click of a button even before hardware is ready. This feature helps us ensure that a very high percentage of our circuits are testable."

As a further benefit, KERAjet is able to work closely with its manufacturing partner Celestica, which uses the XJRunner system from XJTAG to test production boards. XJRunner is a run-time environment system optimised for production. This continuity between development and production eases new product introduction and shortens time to market. Celestica is using XJRunner to check every board after the soldering process, to identify defects that sometimes cannot be detected by conventional optical inspection.

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ture of siness	Specialised inkjet printers for the ceramic industry
in product	Four-colour printers capable of applying digital tile decoration in a single pass
stomers	Ceramic industries of Southern Europe and worldwide
ations	Vila–real, Spain offices in Italy and Portugal
ployees	40
	€12million Invests around €3million in R&D
b site	www.kerajet.com

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