



Flying Probe Innovators Boost Testability and Speed with XJTAG Boundary Scan

“System Europe GmbH sells and supports Takaya flying probe test systems, and has many years’ experience with their versatile built-in test and inspection features. To extend test coverage where components are difficult to probe, System now integrates XJTAG boundary scan, which provides a simple interface and powerful, easy-to-use features.”

Based in Düsseldorf, Germany, and with a network covering more than 20 countries, System Europe supplies advanced electronic assembly, inspection and test equipment to European high-tech OEMs and contract manufacturers, and provides services ranging from test coverage analysis and test program creation to complete outsourced testing and reporting.

The company is responsible for the entire European sales and support network for Takaya flying probe systems, and also bases its test services portfolio on the speed and flexibility of Takaya’s technology. To enhance testing of devices with I/O pins that are difficult or impossible to probe, such as processors or FPGAs in BGA packages, System has integrated XJTAG boundary scan within the Takaya flying probe machines.

“We constantly strive to create superior test solutions for our clients,” comments Jörg Lewandowski, Managing Director, System Europe. “XJTAG boundary scan is an ideal accompaniment to flying probe test, offering great functionality and ease of use, as well as a competitive price and flexible licensing model.”

The combination of flying probe and boundary scan is able to produce a solution with both higher combined test coverage and faster test time, and so System has assisted XJTAG to create an integrated system that multiplies the strengths of the two technologies.

“It is straightforward to connect the JTAG controller module inside the flying probe equipment, and then to run combined flying probe and boundary scan tests from the Takaya software. This further increases test coverage and can be used to ensure faster cycle times,” explains Jörg Lewandowski.

The XJLink2+ controller provides a convenient way to connect the XJTAG boundary scan system to the hardware being tested, and can control up to four JTAG chains. The

software licence can be held inside the XJLink2+ or as a network license, allowing flexibility for the customer. The XJLink2+ connects physically via an I/O expansion board to the Takaya machine in order to access the flying probes. An XJTAG executable is started on the PC by the Takaya software and communicates with the Takaya system whilst running an XJTAG test project.

With an XJLink2+ and I/O expansion board integrated in the Takaya tester, the XJTAG system can move the flying probes and drive signals on them during boundary scan testing. This access to nets that otherwise would not be controllable during boundary scan testing allows increased test coverage because the system can

perform hybrid testing where some pins are accessed by flying probes while others are simultaneously driven or read from the boundary scan system. Perhaps less obviously, some flying probe test steps can be removed because boundary scan tests cover the same nets, which results in less probe movements and an improvement in test time.

“The excellent features of XJTAG boundary scan, and easy access to technical support from product experts in Cambridge, gives us freedom to innovate and create the test systems we really want to deliver for our clients,” summarises Jörg Lewandowski. “The result truly is greater than the sum of the parts, and XJTAG has played a large part in making that possible.”

opinion

Jörg Lewandowski
Managing Director
System Europe GmbH

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Data Bank

SYSTEM EUROPE

Company	System Europe GmbH HQ Germany
Nature of business	Electronics testing and production systems and services provider
Main products	Takaya flying probe testers, CAD conversion, wire bonding
Customers	Electronics and microelectronics manufacturers worldwide
Founded	1989 (as ITOCHU Deutschland GmbH)
Location	Düsseldorf, Germany
Web site	www.system-europe.de

Special Offer

Free Board Setup + Free XJTAG Trial



- Do you design boards with BGAs on?
- Does your hardware include FPGAs, CPLDs, DSPs or microprocessors?
- Would you like to debug your boards, detect faults and prove your design quickly and easily?

Well, this is where **XJTAG Boundary Scan** can help by offering you a **FREE 30-day trial of XJTAG on your own board.**

Yes, that's right, XJTAG will set up its test system on your board for free when you take a 30-day trial.

Apply today and discover how XJTAG can help you save time and money

Find out why leading companies are using XJTAG

“XJTAG is an absolute necessity for any company designing complex circuits that feature high pin count BGA or chip scale devices.”

“XJTAG is easy to use and incredibly fast, which has enabled us to shave weeks off the development schedule for our RFeye module thereby freeing our development team from time-consuming debugging tasks.”

Alistair Massarella, CEO – CRFS

ARM Case Study



ARM selects XJTAG for RealView development tools debug and test

ARM, the world's leading semiconductor intellectual property (IP) supplier, has reduced the time and cost of developing its range of RealView® development tools by using the XJTAG boundary scan development system to improve and speed up the process of debugging and testing its high density, multi-layer development boards.

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